

IN THE CLAIMS

Claim 1. (Currently Amended) An MPEG video decoder for an MPEG bit stream into which ~~a picture is~~ a plurality of pictures and a plurality of parameters of each layer are encoded, the parameters including a parameter of a sequence layer representing any one of a horizontal size and a vertical size of the pictures the MPEG video decoder comprising:

~~an image decoding section which decodes the MPEG bit stream to obtain the pictures and the parameters of each layer, wherein the parameters include parameters of a sequence layer, the parameters of the sequence layer includes a horizontal size value and a vertical size value of the picture, and the image decoding section includes an internal buffer for temporarily storing the pictures and the parameters;~~

~~a frame memory having a plurality of banks and which is connected to the image decoding section, wherein each of the banks has an area for storing the picture and the parameters obtained by the image decoding section, and stores the picture and the parameters by mutually relating the picture and the parameters as a set and includes~~
a first picture bank which stores a first picture currently obtained by the image decoding section;

a first parameter bank which is associated with the first picture bank and stores first parameters for displaying the first picture;

a second picture bank which stores a second picture obtained by the image decoding section immediately before the first picture; and

a second parameter bank which is associated with the second picture bank and stores second parameters for displaying the second picture;

a decode control section which controls the image decoding section; and
a display control section which is connected to the decode control section and to
the frame memory, ~~wherein the display control section~~ and carries out a display control
of the first picture and the second picture stored in the frame memory based on the
~~parameters stored in the frame memory; and the first parameters and the second~~
parameters, respectively, wherein
~~a status register for storing values indicating whether display of the picture stored~~
~~in the frame memory has finished, wherein the status register has an arbitration function~~
~~for arbitrating between the decode control section and the display control section~~
the first parameters and the second parameters include an identical parameter of
the sequence layer.

Claim 2. (Currently Amended) The MPEG video decoder according to claim 1,
~~wherein~~ further comprising

~~the a status register which indicates a data storage state of each bank a status of~~
the first picture bank and the second picture bank, and wherein

the decode control section updates the status register when any one of the first
picture and the second picture is obtained, and the display control section updates the
status register when any one of the first picture and the second picture is displayed.

Claim 3 (Currently Amended) The MPEG video decoder according to claim 1,
wherein the image decoding section includes the an internal buffer that temporarily
stores the first pictures and the first parameters in a macro block unit.

Claim 4. (Canceled)

Claim 5. (Currently Amended) The MPEG video decoder according to claim 3, ~~wherein~~ further comprising a data transfer path for transferring the first picture, the first parameters, the second picture, and the second parameters ~~from between~~ the internal buffer ~~to and~~ the frame memory ~~also works as a data transfer path for transferring the parameters of each layer between the internal buffer and the frame memory.~~

Claim 6. (Currently Amended) The MPEG video decoder according to ~~claim 4~~ claim 3, wherein the image decoding section reads out the second parameters from the second parameter bank into the internal buffer, decodes the MPEG bit stream to obtain the first picture and a parameter ~~first parameters, and overwrites~~ obtaining the first parameters by overwriting a part of the second parameters in the internal buffer ~~obtained immediately before with~~ the parameter, and write the first parameters into the first parameter bank.

Claim 7. (Previously Presented) The MPEG video decoder according to claim 1, wherein the decode control section operates asynchronously with a vertical synchronization signal, and the display control section operates in synchronism with the vertical synchronization signal.

Claim 8. (Previously Presented) The MPEG video decoder according to claim 2, wherein the display control section does not update the status register when a reference picture of other pictures is displayed.

Claim 9. (Currently Amended) An MPEG video decoding method for an MPEG bit stream into which ~~a first picture and a second picture~~ a plurality of pictures and a plurality of parameters of each layer are encoded, the parameters including a parameter of a sequence layer representing any one of a horizontal size and a vertical size of the pictures, the MPEG video decoding method comprising:

~~reading first parameters of each layer relating to the first picture, wherein the first parameters include parameters of a sequence layer, and the parameters of the sequence layer includes a horizontal size value and a vertical size value of the first picture for displaying a first picture from a frame memory;~~

decoding the MPEG bit stream to obtain a second picture and a parameter;

~~obtaining second parameters of each layer relating to the second picture, wherein the second parameters include parameters of a sequence layer, and the parameters of the sequence layer includes a horizontal size value and a vertical size value of the second picture;~~

for displaying the second picture by overwriting a part of the first parameters with the ~~second parameters~~ parameter;

storing the second picture and the second parameters into [[a]] the frame memory[[;]]

~~obtaining the second picture by decoding the MPEG bit stream;~~

~~storing the second picture into the frame memory so as to be combined with the~~
~~second parameters as a set, by relating the second picture to the second parameters;~~
~~controlling the display of the second picture based on the second parameters of~~
~~each layer stored in the frame memory;~~
~~storing values in a status register, the values indicating whether display of the~~
~~second picture stored in the frame memory has finished; and~~
~~arbitrating between the obtaining of the second picture and the second~~
~~parameters and the controlling of display, wherein~~
the first parameters and the second parameters include an identical parameter of
the sequence layer.

Claim 10. (Currently Amended) The MPEG video decoding method according to claim 9, wherein ~~at a start of decoding of the MPEG bit stream, the second~~ parameters are read from a memory area that is to store the second parameters.

Claim 11. (Previously Presented) The MPEG decoder according to claim 1, wherein the layer includes at least one layer of a GOP layer and a picture layer.

Claim 12. (Previously Presented) The MPEG decoding method according to claim 9, wherein the layer includes at least one layer of a GOP layer and a picture layer.